



2811

Attorney's Docket No.: 10417-119001 / F51-143213M/SW

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Yoshinori Hino et al. Art Unit : 2811
Serial No. : 10/080,831 Examiner : J. Nguyen
Filed : February 21, 2002
Title : SEMICONDUCTOR DEVICE AND PATTERN LAYOUT METHOD THEREOF

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT IN REPLY TO ACTION OF AUGUST 20, 2003

Please amend the above-identified application as follows:

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I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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